



HSMM4R8N200

General Description

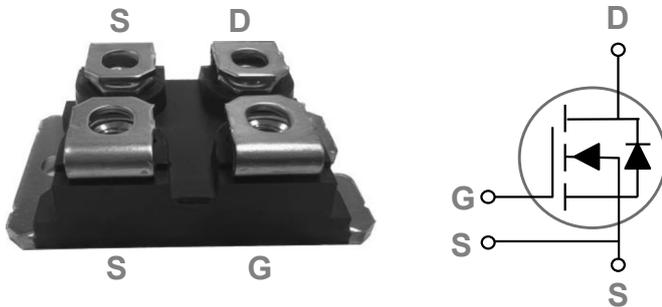
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	R _{DS(ON)}	I _D
200V	4.8mΩ	230A

Features

- 200V,230A, R_{DS(ON)} =4.8mΩ @V_{GS} = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

SOT-227 Pin Configuration



Absolute Maximum Ratings T_c=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	200	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Drain Current – Continuous (T _C =25°C)	230	A
	Drain Current – Continuous (T _C =100°C)	147	A
I _{DM}	Drain Current – Pulsed ¹	920	A
EAS	Single Pulse Avalanche Energy ²	4.5	J
IAS	Single Pulse Avalanche Current ²	95	A
P _D	Power Dissipation (T _C =25°C)	695	W
	Power Dissipation – Derate above 25°C	5.6	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJC}	Thermal Resistance Junction to Case	---	0.18	°C/W



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Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	200	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=200V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	3	μA
		$V_{DS}=160V, V_{GS}=0V, T_J=85^\circ\text{C}$	---	---	30	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=150A$	---	4	4.8	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	2.5	3.5	4.5	V

Dynamic and switching Characteristics³

Q_g	Total Gate Charge	$V_{DS}=100V, V_{GS}=10V, I_D=120A$	---	250	---	nC
Q_{gs}	Gate-Source Charge		---	56	---	
Q_{gd}	Gate-Drain Charge		---	72	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=100V, V_{GS}=10V, R_G=6\Omega, I_D=120A$	---	50	---	ns
T_r	Rise Time		---	80	---	
$T_{d(off)}$	Turn-Off Delay Time		---	100	---	
T_f	Fall Time		---	120	---	
C_{iss}	Input Capacitance	$V_{DS}=100V, V_{GS}=0V, F=1\text{MHz}$	---	14000	---	pF
C_{oss}	Output Capacitance		---	1000	---	
C_{rss}	Reverse Transfer Capacitance		---	48	---	
R_g	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1\text{MHz}$	---	0.8	---	Ω

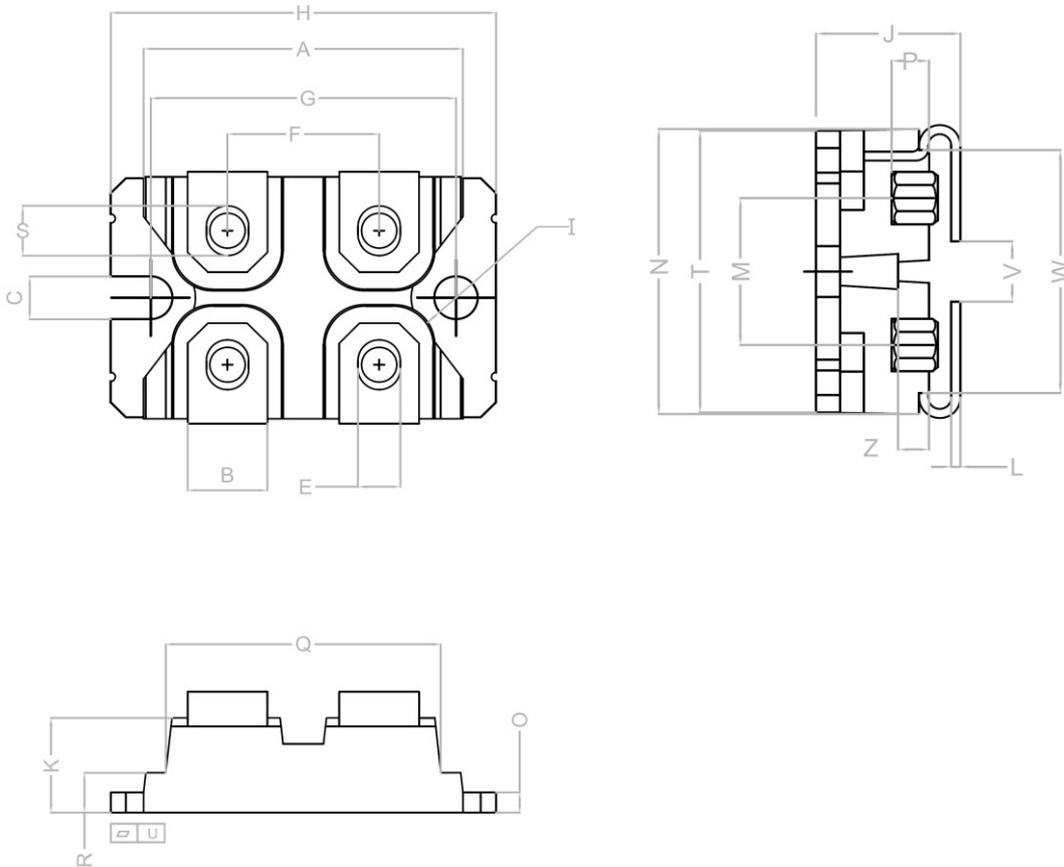
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V, \text{Force Current}$	---	---	230	A
I_{SM}	Pulsed Source Current		---	---	460	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_S=100A, T_J=25^\circ\text{C}$	---	---	1.5	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=50V, V_{GS}=10V, L=1\text{mH}, I_{AS}=95A, R_G=25\Omega, \text{Starting } T_J=25^\circ\text{C}.$
3. Essentially independent of operating temperature.

SOT-227 PACKAGE INFORMATION



SYMBOL	mm		SYMBOL	mm	
	MIN	MAX		MIN	MAX
A	31.40	31.60	N	24.40	25.00
B	7.70	8.10	O	1.90	2.10
C	4.20	4.40	P	2.92	3.32
D	4.20	4.40	Q	26.60	27.00
E	4.10	4.40	R	3.80	4.20
F	14.90	15.10	S	4.95	5.45
G	30.10	30.30	T	23.70	24.30
H	38.00	38.40	U	0.00	0.10
J	12.00	12.60	V	3.50	5.50
K	9.35	9.65	W	20.15	20.45
L	0.74	0.84	Z	2.50	2.70
M	12.30	12.70			